



520.43306X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Osamu SUZUKI, et al.
Serial No: 10/722,597
Filed: November 28, 2003
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEROF
Group: 2894
Examiner: Monica LEWIS

**SUBMISSION INCLUDING AMENDMENT
PURSUANT TO 37 C.F.R. §1.114**

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 17, 2009

Sir:

This Submission is an Amendment being filed in connection with an RCE (Request for Continued Examination), which is further to the filing of the Notice of Appeal on June 15, 2009, and is responsive to the Final Office Action, dated December 15, 2008. (An RCE Transmittal, along with credit card authorization payment covering the fee amount directed thereto, is enclosed.) Please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks and are included following the amendments.

Appendix Sketch A